

N-channel SiC power MOSFET

V_{DSS}	650V
R _{DS(on)} (Typ.)	120m $Ω$
I _D	21A
P_D	103W

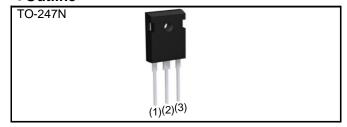
Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

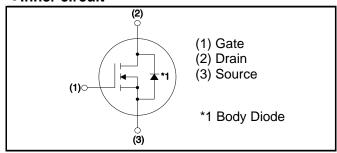
Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

Outline



●Inner circuit



Packaging specifications

	<u> </u>	
Pac	Packing	Tube
	Reel size (mm)	-
Type	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3120AL

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	650	V	
Continuous drain current	T _c = 25°C	I _D *1	21	А
Continuous drain current	T _c = 100°C	I _D *1	15	А
Pulsed drain current	I _{D,pulse} *2	52	А	
Gate - Source voltage	V_{GSS}	-4 to 22	V	
Junction temperature	T _j	175	°C	
Range of storage temperature	T _{stg}	-55 to +175	°C	

●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	1.12	1.46	°C/W

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
raiainetei	er Symbol C		Conditions Min.		Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	650	-	-	V
		$V_{DS} = 650 V, V_{GS} = 0 V$				
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	1	10	μΑ
didiii odiioiit		T _j = 150°C	-	2	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_{D} = 3.33 \text{mA}$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 6.7A$				
Static drain - source on - state resistance	R _{DS(on)} *3	T _j = 25°C	-	120	156	mΩ
2 2		T _j = 125°C	-	158.4	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	18	-	Ω

●Electrical characteristics (T_a = 25°C)

Doromotor	Symbol Conditions		Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g _{fs} *3	$V_{DS} = 10V, I_D = 6.7A$	-	2.7	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	460	-	
Output capacitance	C _{oss}	V _{DS} = 500V	-	35	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	16	-	
Effective output capacitance, energy related	$C_{\text{o(er)}}$	$V_{GS} = 0V$ $V_{DS} = 0V$ to 300V	,	70	-	pF
Turn - on delay time	t _{d(on)} *3	$V_{DD} = 300V, I_D = 6.7A$	ı	14	-	
Rise time	t _r *3	V _{GS} = 18V/0V	ı	21	ı	nc
Turn - off delay time	t _{d(off)} *3	$R_L = 45\Omega$	1	23	1	ns
Fall time	t _f *3	$R_G = 0\Omega$	ı	14	ı	
Turn - on switching loss	E _{on} *3	$V_{DD} = 300V, I_{D} = 6.7A$ $V_{GS} = 18V/0V$	-	29		1
Turn - off switching loss	E _{off} *3	$R_G = 0\Omega L=500\mu H$ * E_{on} includes diode reverse recovery	-	3	-	μJ

•Gate Charge characteristics ($T_a = 25$ °C)

Dorometer	Parameter Symbol	Conditions	Values			Unit
- Farameter			Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*3}	$V_{DD} = 300V$	-	38	ı	
Gate - Source charge	Q_{gs}^{*3}	$I_{D} = 6.7A$	-	11	-	nC
Gate - Drain charge	Q_{gd}^{*3}	V _{GS} = 18V	-	13	-	
Gate plateau voltage	V _(plateau)	$V_{DD} = 300V, I_D = 6.7A$	-	9.6	-	V

^{*1} Limited only by maximum temperature allowed.

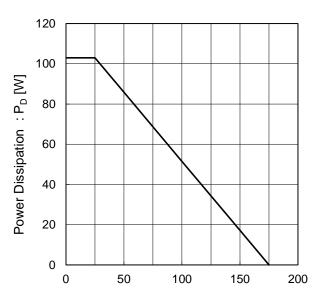
^{*2} PW \leq 10 $\mu s, \ Duty \ cycle \leq$ 1%

^{*3} Pulsed

ullet Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

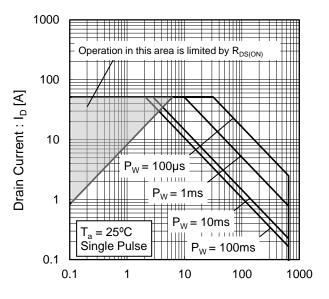
Parameter	Symbol	Conditions	Values			Unit	
Parameter Symbol		Conditions	Min.	Тур.	Max.	Offic	
Inverse diode continuous, forward current	l _S *1	T _c = 25°C	-	-	21	А	
Inverse diode direct current, pulsed	I _{SM} *2	1 _c = 25°C	-	-	52	А	
Forward voltage	V _{SD} *3	$V_{GS} = 0V, I_{S} = 6.7A$	-	3.2	-	V	
Reverse recovery time	t _{rr} *3		-	13	-	ns	
Reverse recovery charge	Q _{rr} *3	I _F = 6.7A, V _R = 300V di/dt = 1100A/μs	-	35	-	nC	
Peak reverse recovery current	I _{rrm} *3		-	6	-	Α	

Fig.1 Power Dissipation Derating Curve

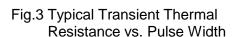


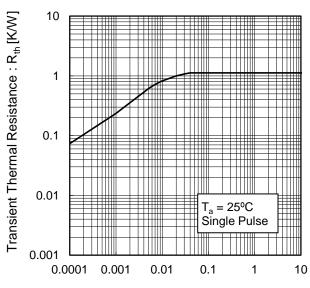
Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area



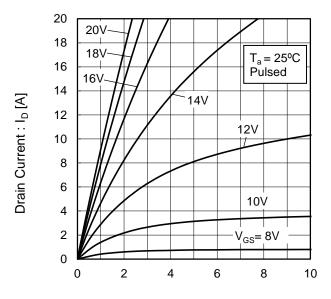
Drain - Source Voltage : V_{DS} [V]





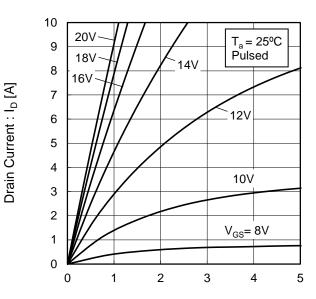
Pulse Width : P_W [s]

Fig.4 Typical Output Characteristics(I)

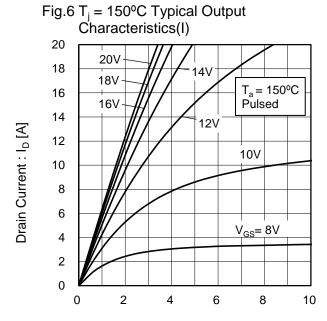


Drain - Source Voltage : V_{DS} [V]

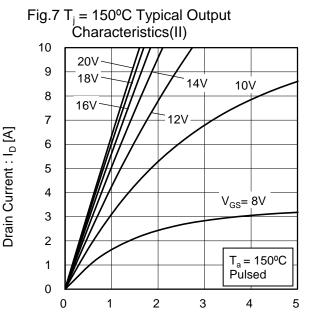
Fig.5 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

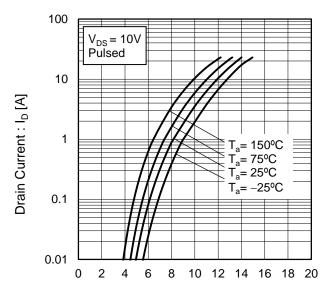


Drain - Source Voltage : V_{DS} [V]



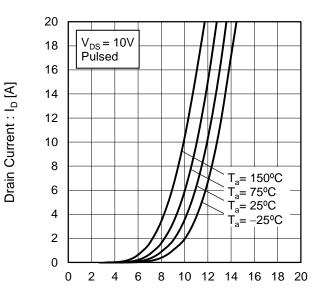
Drain - Source Voltage : V_{DS} [V]

Fig.8 Typical Transfer Characteristics (I)



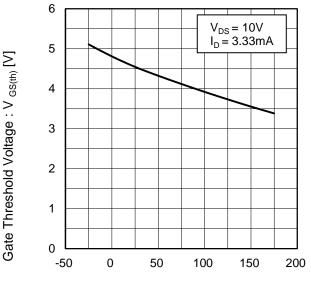
Gate - Source Voltage : V_{GS} [V]

Fig.9 Typical Transfer Characteristics (II)



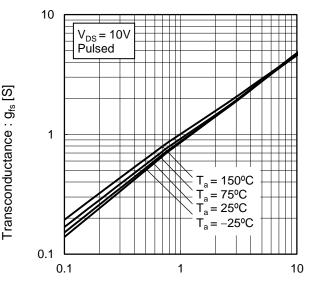
Gate - Source Voltage : $V_{GS}[V]$

Fig.10 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T_i [°C]

Fig.11 Transconductance vs. Drain Current



Drain Current : I_D [A]

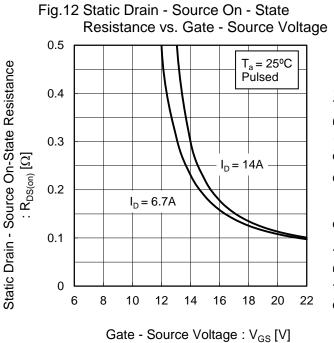
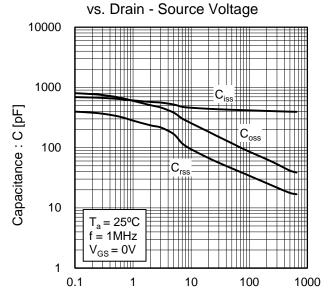


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature 0.5 V_{GS} = 18V Pulsed Static Drain - Source On-State Resistance 0.4 0.3 $R_{\text{DS(on)}}\left[\Omega\right]$ 0.2 $I_D = 14A$ 0.1 $I_{D} = 6.7A$ 0 -50 0 50 100 150 200

Junction Temperature : T_i [°C]

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current Static Drain - Source On-State Resistance $: R_{DS(on)} \left[\Omega \right]$ 0.1 = 150°C = 125°C T_a = 75°C $= 25^{\circ}C$ -25°C V_{GS} = 18V Pulsed 0.01 10

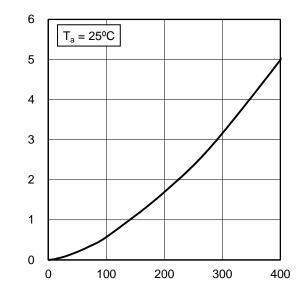
Fig.15 Typical Capacitance



Drain - Source Voltage : V_{DS} [V]

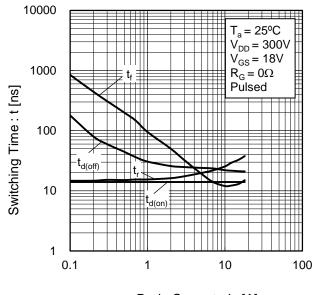
Fig.16 Coss Stored Energy

Coss Stored Energy : $\mathsf{E}_{\mathsf{OSS}}$ [$\mu \mathsf{J}$]



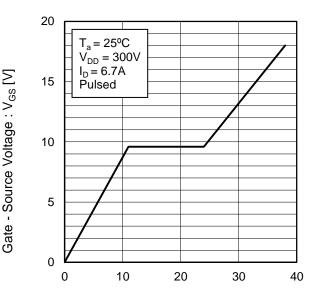
Drain - Source Voltage : V_{DS} [V]

Fig.17 Switching Characteristics



Drain Current : I_D [A]

Fig.18 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]

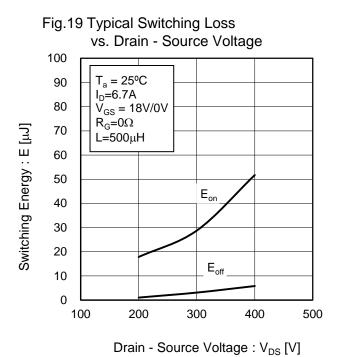
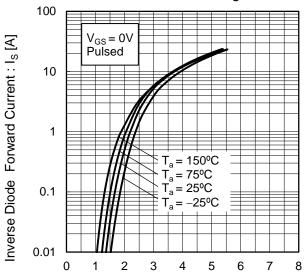


Fig.20 Typical Switching Loss vs. Drain Current 200 $T_a = 25^{\circ}C$ $V_{DD} = 300V$ $V_{GS} = 18V/0V$ $R_G = 0\Omega$ Switching Energy : E [µJ] 150 L=500uH 100 E_{on} 50 $\mathsf{E}_{\mathsf{off}}$ 0 0 5 10 15 20 Drain Current: I_D [A]

Fig.21 Typical Switching Loss vs. External Gate Resistance 200 $T_a = 25^{\circ}C$ V_{DD}=300V $I_{D} = 6.7A$ 150 $V_{GS} = 18V/0V$ Switching Energy : E [µJ] L=500μH 100 E_{on} 50 $\mathsf{E}_{\mathsf{off}}$ 0 5 0 10 15 20 25 30

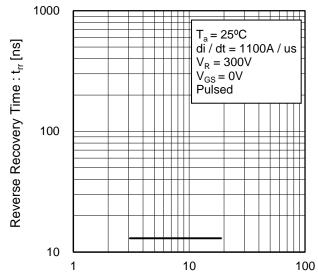
External Gate Resistance : $R_G[\Omega]$

Fig.22 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.23 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

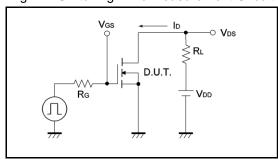


Fig.2-1 Gate Charge Measurement Circuit

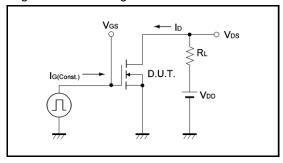


Fig.3-1 Switching Energy Measurement Circuit

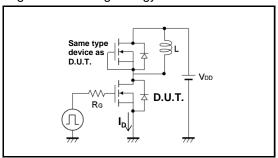


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

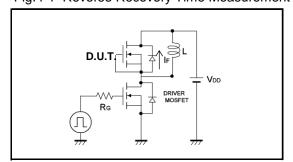


Fig.1-2 Switching Waveforms

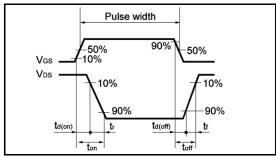


Fig.2-2 Gate Charge Waveform

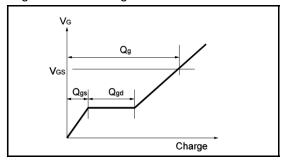
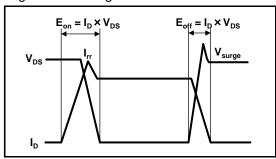
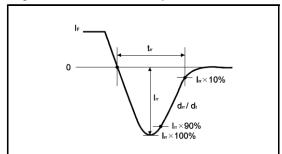


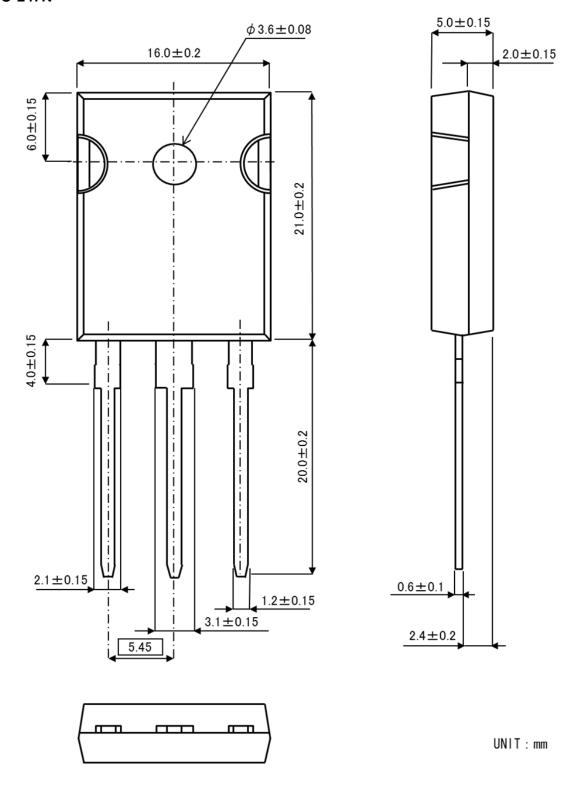
Fig.3-2 Switching Waveforms





Dimensions

TO-247N



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SCT3120AL - Web Page

Distribution Inventory

Part Number	SCT3120AL
Package	TO-247N
Unit Quantity	450
Minimum Package Quantity	30
Packing Type	Tube
Constitution Materials List	inquiry
RoHS	Yes