



P-Channel 60 V (D-S) MOSFET

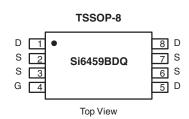
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 60	0.115 at V _{GS} = - 10 V	- 2.7		
	0.150 at V _{GS} = - 4.5 V	- 2.4		

FEATURES

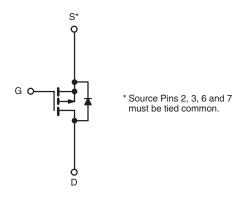
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si6459BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	S (T _A = 25 °C, unle	ess otherwise	noted)		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 60		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 2.7	- 2.2	
	T _A = 70 °C		- 2.2	- 1.8	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	- 20		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.25	- 0.83	
Avalanche Current	L = 0.1 mH	I _{AS}	15		
Single Pulse Avalanche Energy	L=0.1 mn	E _{AS}	11		mJ
Mariana Barra Biraira tira a	T _A = 25 °C	P _D	1.50	1.0	W
Maximum Power Dissipation ^a	T _A = 70 °C	' D	1.0	0.67	VV
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian de Ambienta	t ≤ 10 s	- R _{thJA}	66	83	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		100	120		
Maximum Junction-to-Foot	Steady State		50	60		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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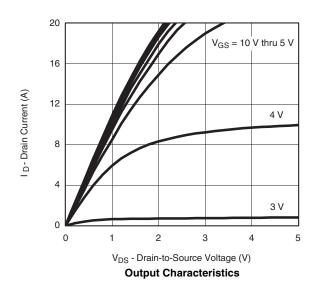
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V			- 1	μА	
		V_{DS} = - 60 V, V_{GS} = 0 V, T_{J} = 70 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 2.7 A		0.092	0.115	Ω	
		$V_{GS} = -4.5 \text{ V}, I_D = -2.4 \text{ A}$		0.120	0.150		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 2.7 A		8		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.25 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			14.5	22		
Gate-Source Charge	Q_{gs}	V_{DS} = - 30 V, V_{GS} = - 10 V, I_D = - 2.7 A		2.2		nC	
Gate-Drain Charge	Q_{gd}			3.7		1	
Gate Resistance	R_{g}			14		Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = - 30 V, R_L = 30 Ω		15	22		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, $V_{GEN}=$ - 10 V, $R_g=6~\Omega$		50	75	ns	
Fall Time	t _f			35	55		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.25 A, dl/dt = 100 A/μs		30	50		

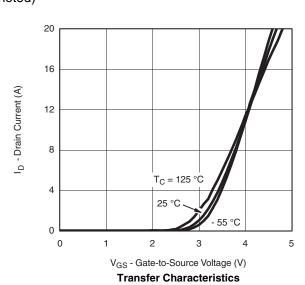
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

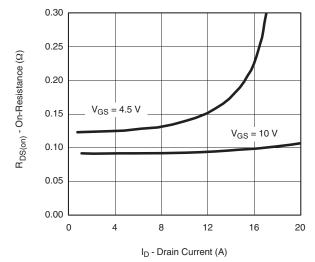




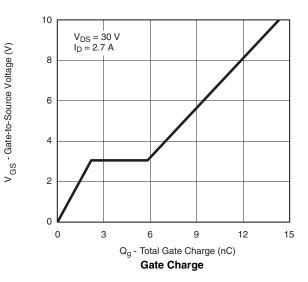


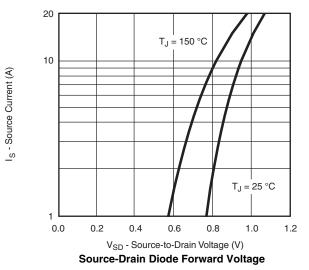


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Drain Current

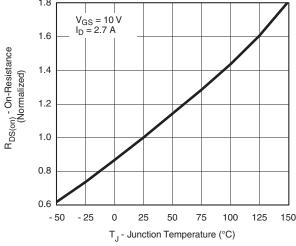




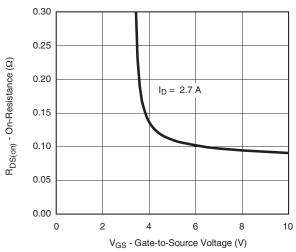
1000 800 C - Capacitance (pF) C_{iss} 600 400 200 C_{oss} $\mathsf{C}_{\mathsf{rss}}$ 0 20 0 10 30 40 50 60 V_{DS} - Drain-to-Source Voltage (V)

1.8 $V_{GS} = 10 \text{ V}$ $I_D = 2.7 \text{ A}$ I_D 1.6

Capacitance



On-Resistance vs. Junction Temperature

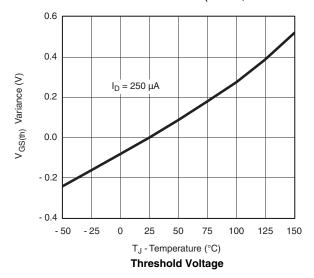


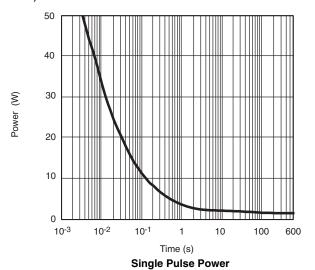
On-Resistance vs. Gate-to-Source Voltage

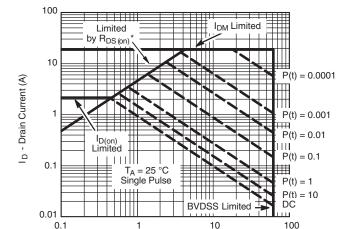
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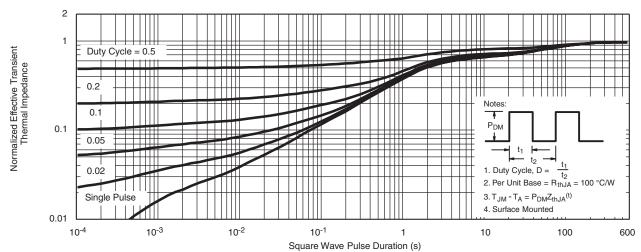
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





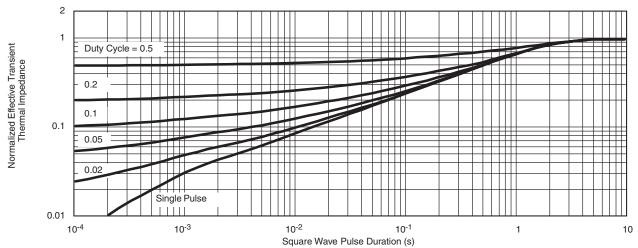


$$\begin{split} &V_{DS}\text{ - Drain-to-Source Voltage (V)}\\ ^*V_{GS}> &\min \text{minmum }V_{GS}\text{ at which }R_{DS(on)}\text{ is specified}\\ &\textbf{Safe Operating Area, Junction-to-Case} \end{split}$$



Normalized Thermal Transient Impedance, Junction-to-Ambient

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

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